### EV026160662US



Zhongze Wang

Title:

Silicon-On-Insulator Comprising Integrated Circuitry and Methods of

Forming Silicon-On-Insulator Circuitry

Assignee:

Micron Technology, Inc.

## INFORMATION DISCLOSURE STATEMENT

References - See Attached Form PTO-1449

The attached form PTO-1449 is submitted in compliance with 37 CFR § 1.56. Copies of the cited art are included. No admission is made regarding whether all the submitted references are prior art.

Respectfully submitted,

Dated: 1/2/200Z Inventor:

Dated: /-/6-02

Attorne Mark S. Matkin

Reg. No. 32,268

Form	PTO-1449

# U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE

ATTY, DOCKET NO.	SERIAL NO.
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#### LIST OF ART CITED BY APPLICANT

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APPLICANT Micron Technology, Inc.					
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### U.S. PATENT DOCUMENTS

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\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered linclude copy of this form with next communication to applicant